



ITW

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the application of:

Group Art Unit: 2661

Govind MALALUR et al.

Examiner: Brian D. NGUYEN

Serial Number: 09/841,189

Atty. Docket No.: 58268.00000

Filed: April 25, 2001

For: GIGABIT SWITCH ON CHIP ARCHITECTURE

**RESPONSE UNDER 37 CFR § 1.111**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 18, 2004

Sir:

In response to the Office Action dated August 11, 2004, please amend the above-identified application as set forth below.

**Amendments to the Drawings are submitted on page 2.**

**Remarks are submitted on page 3.**